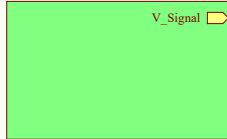
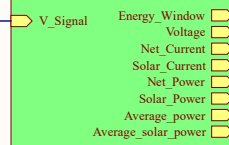


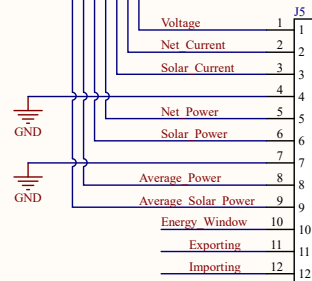
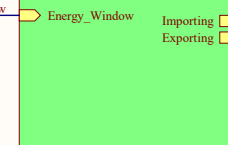
U_Power
Power.SchDoc



U_Analogue
Analogue.SchDoc

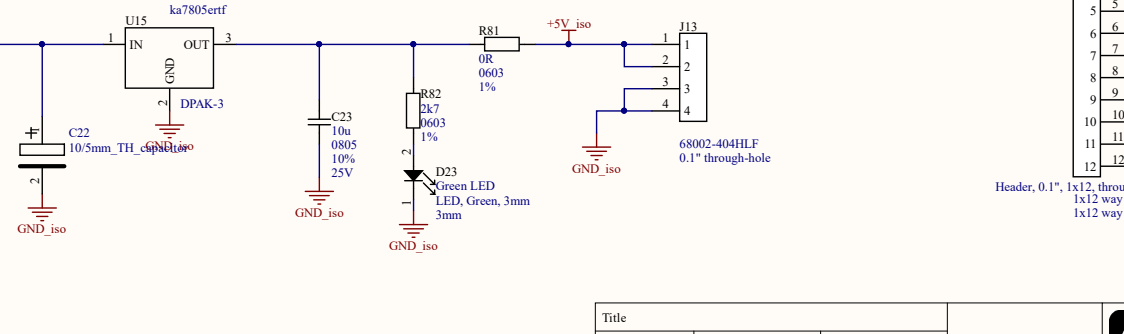
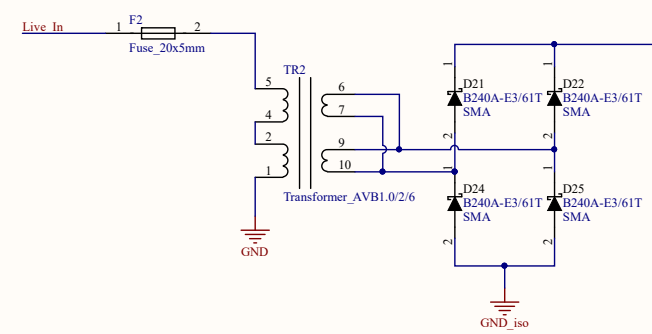
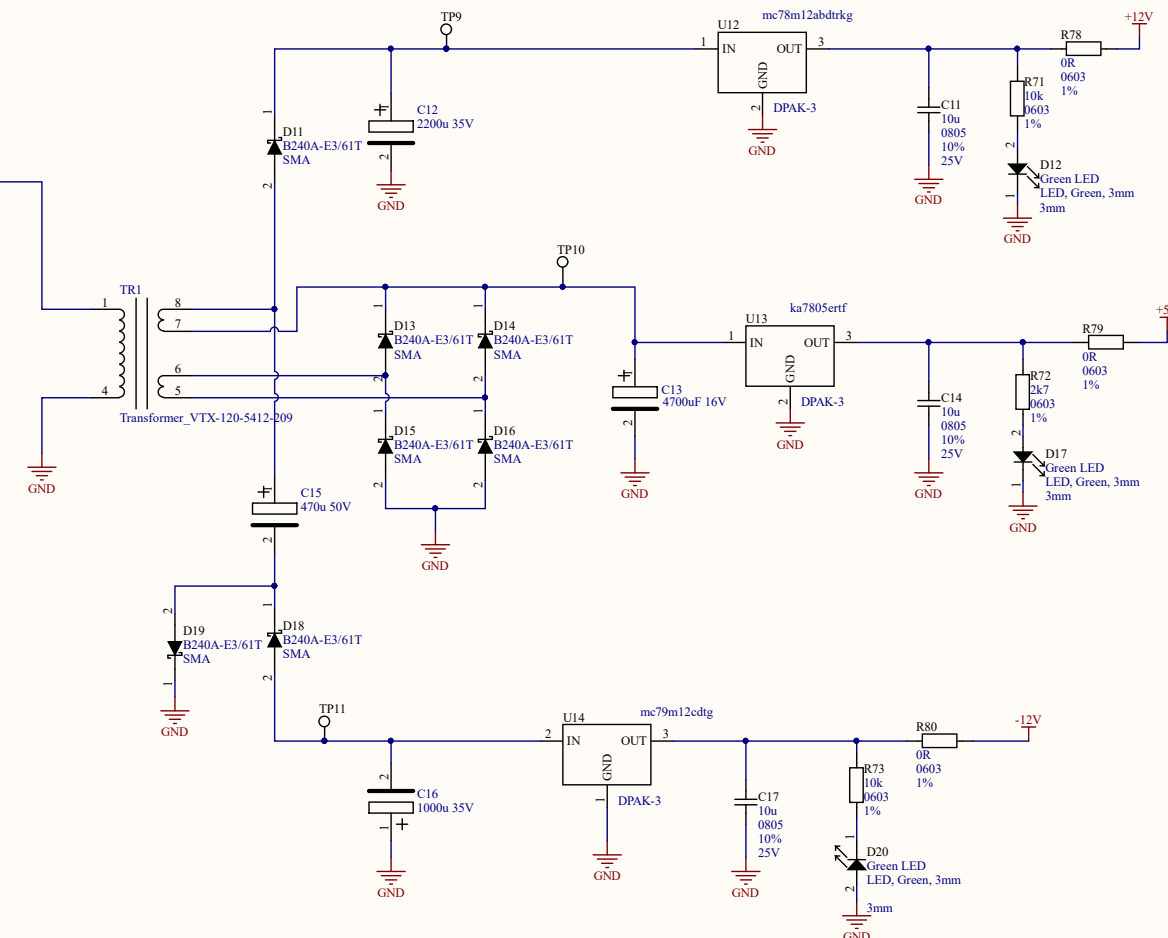
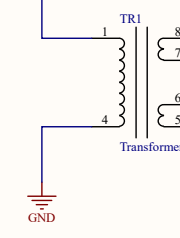
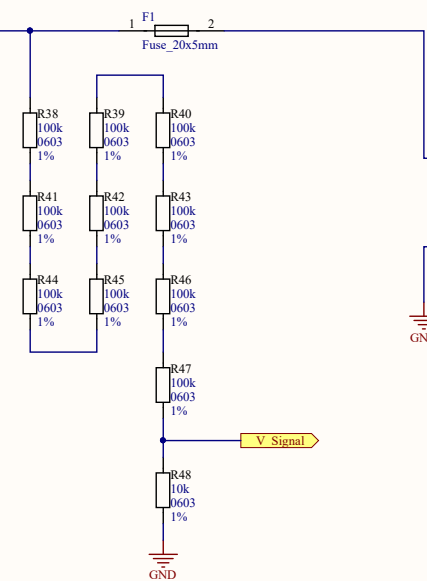
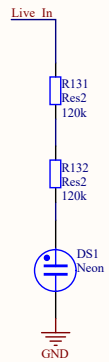
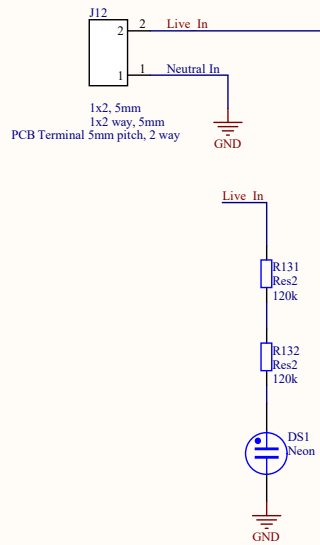


U_Outputs
Outputs.SchDoc

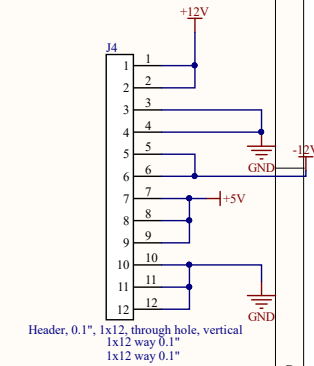


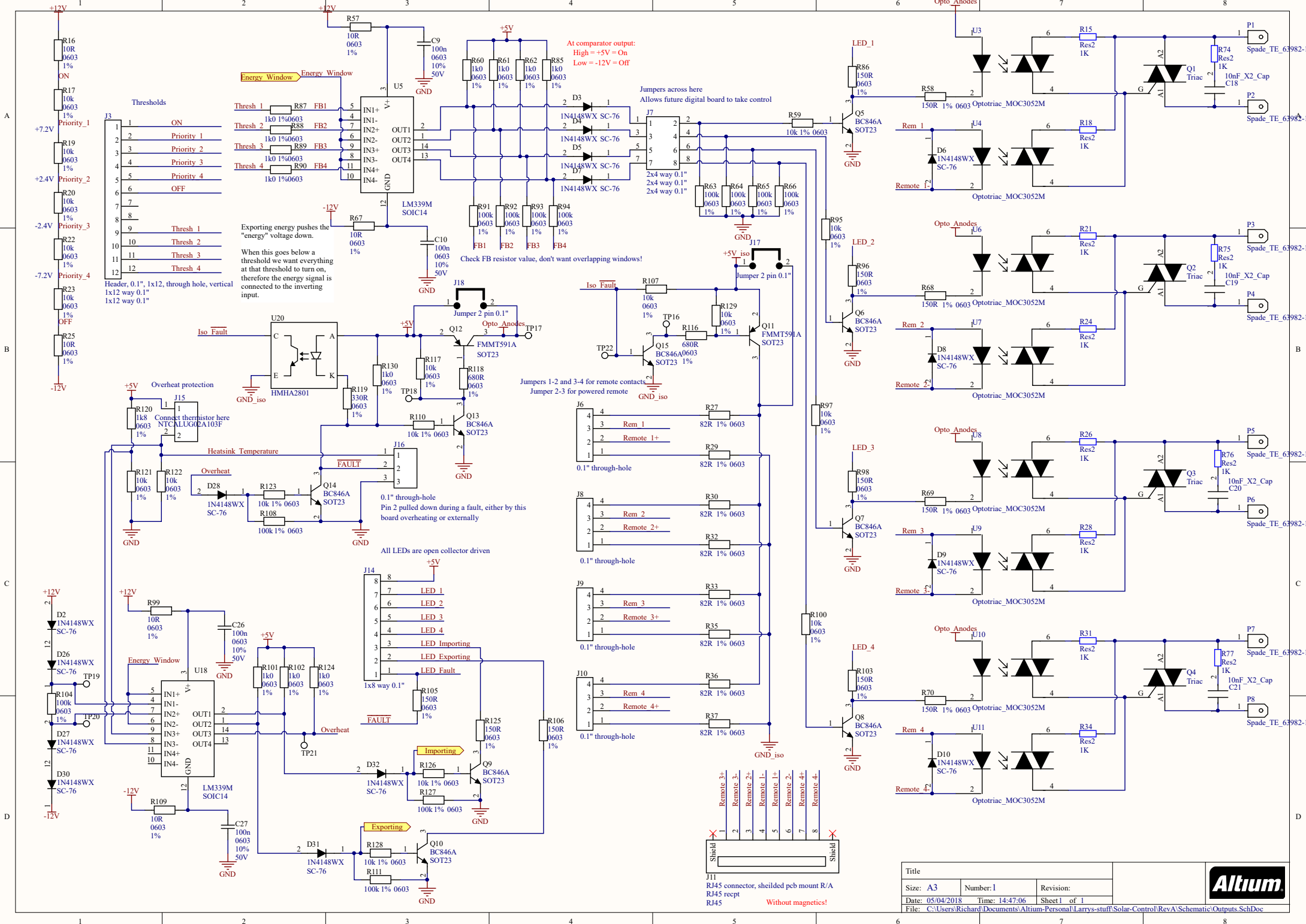
Header to connect to future digital board

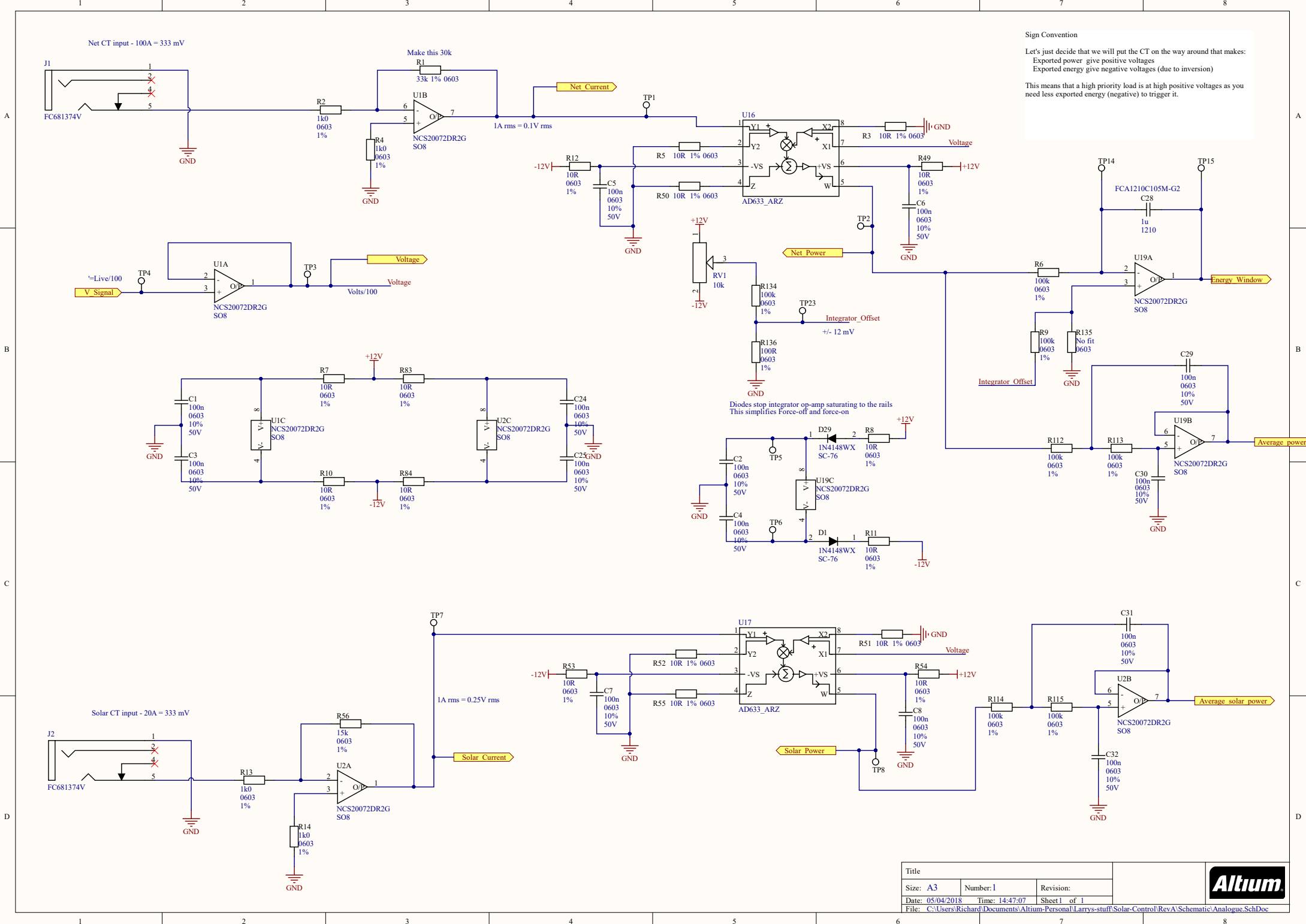
Header, 0.1", 1x12, through hole, vertical
1x12 way 0.1"
1x12 way 0.1"



Power header. Note GND is neutral-reference GND







Sign Convention

Let's just decide that we will put the CT on the way around that makes:
 Exported power give positive voltages
 Exported energy give negative voltages (due to inversion)

This means that a high priority load is at high positive voltages as you need less exported energy (negative) to trigger it.